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# U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10033792	FILING DATE 01/03/2002	CLASS 435 257	SUBCLASS 533	GAU 3681 2615	EXAMINER CAI, WAYNE
<b>**APPLICANTS:</b> Oono Ikuya; Matsui Toshiki; Oosawa Hirotaka;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-005574 01/12/2001					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		XA-9597	
Verified and Acknowledged Examiners's initials		WAC			
<b>TITLE :</b> Signal processing semiconductor integrated circuit device and wireless communication system <small>U.S. DEPT. OF COMM./PAT. &amp; TM-PTO-435L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G
Assistant Examiner		<b>DRAWING</b>	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Primary Examiner		Application Examiner	
<b>PREPARED FOR ISSUE</b>			
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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